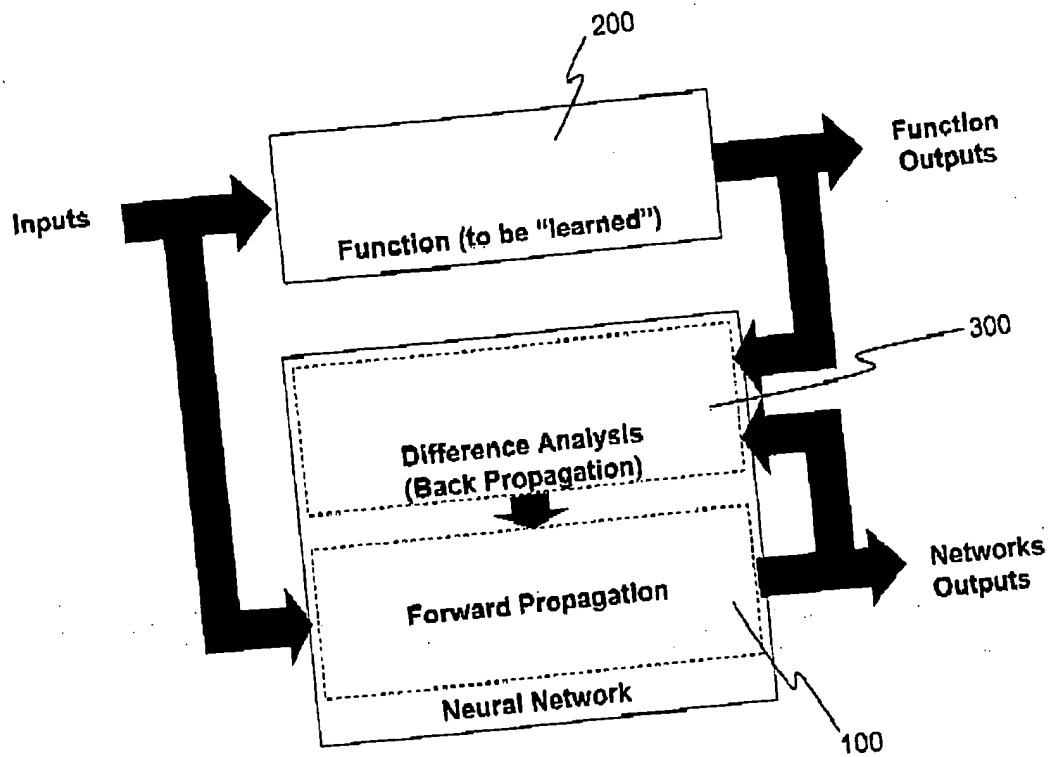
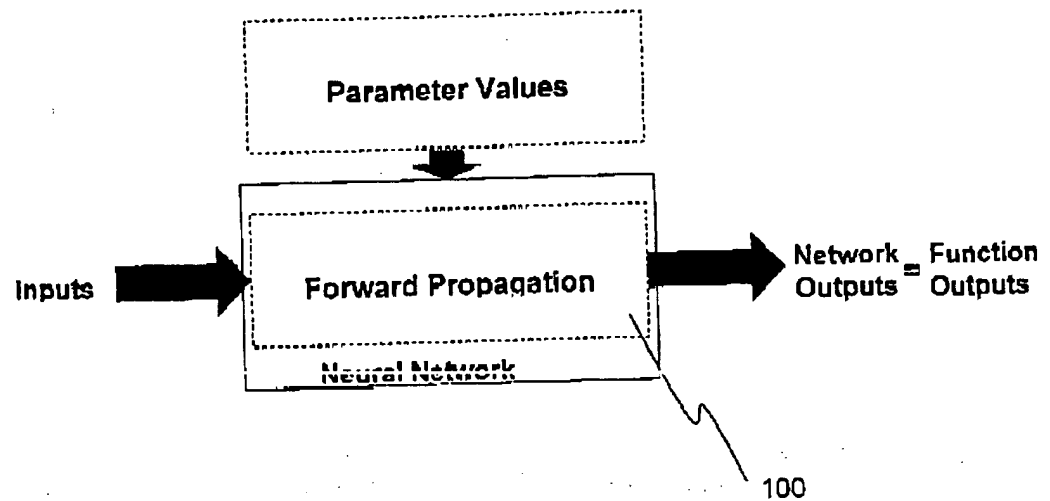


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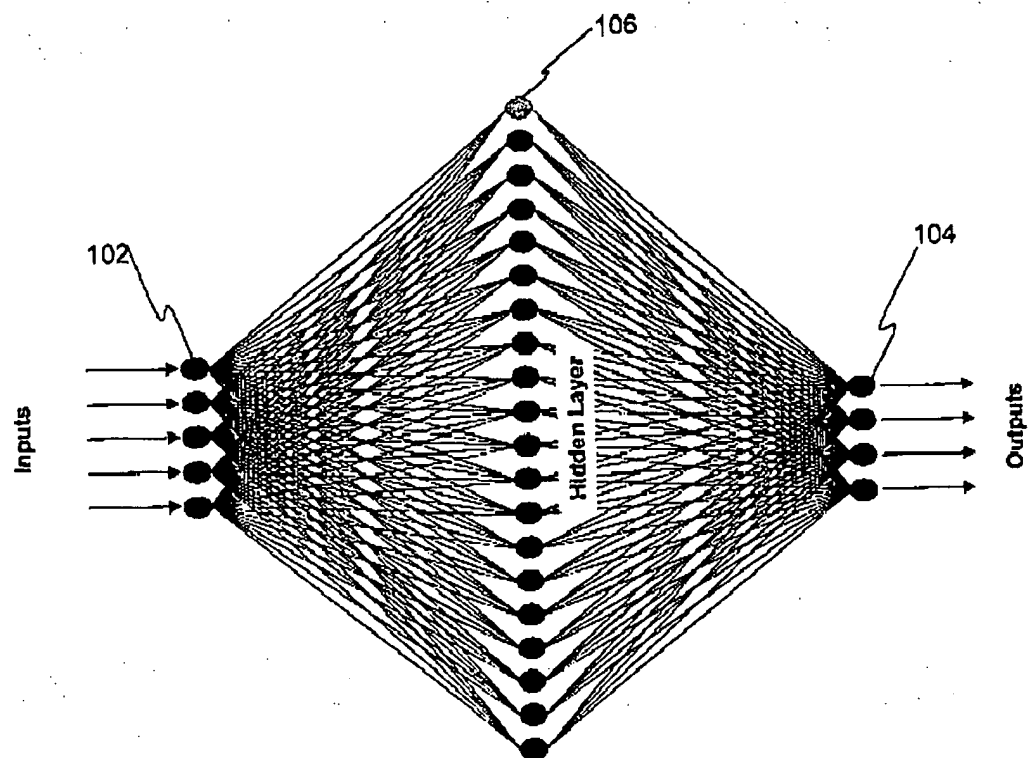
**FIG. 1**

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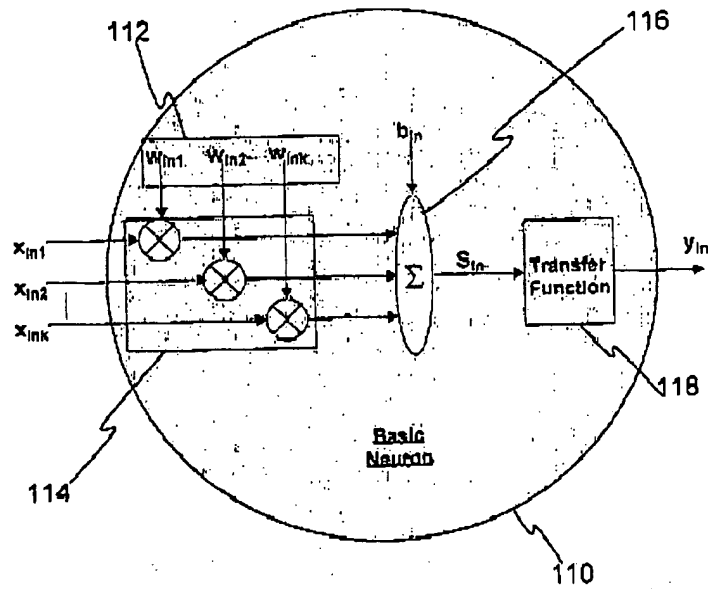


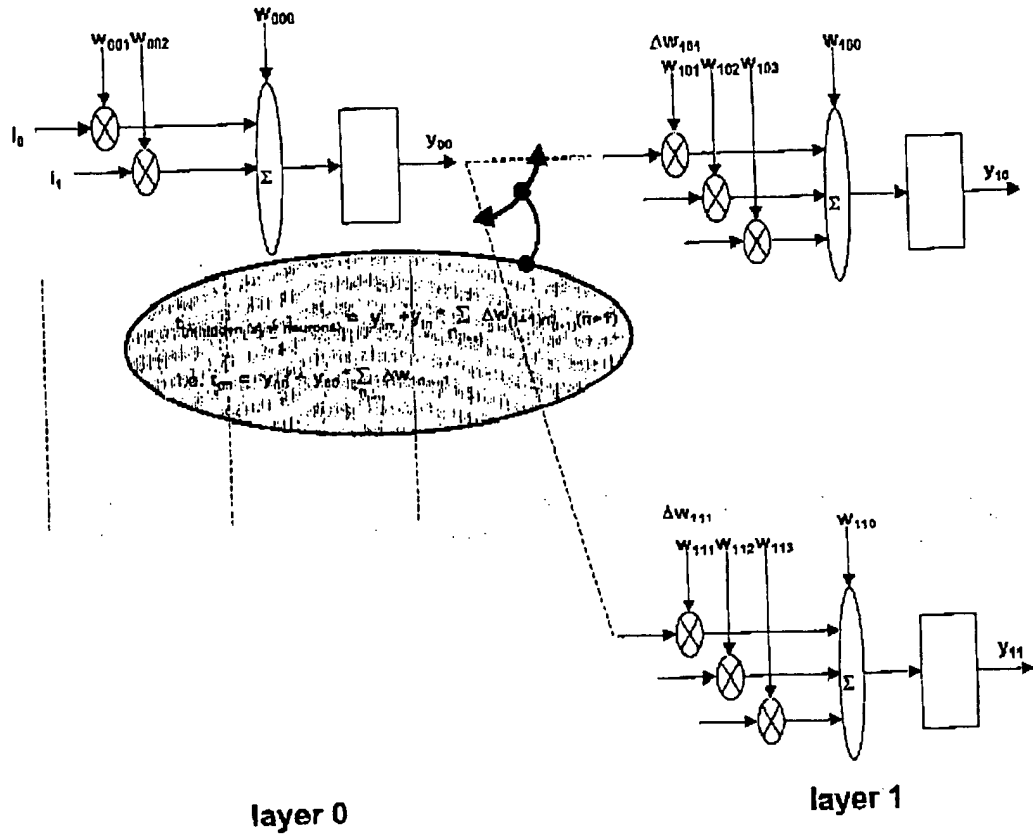
**FIG. 2**

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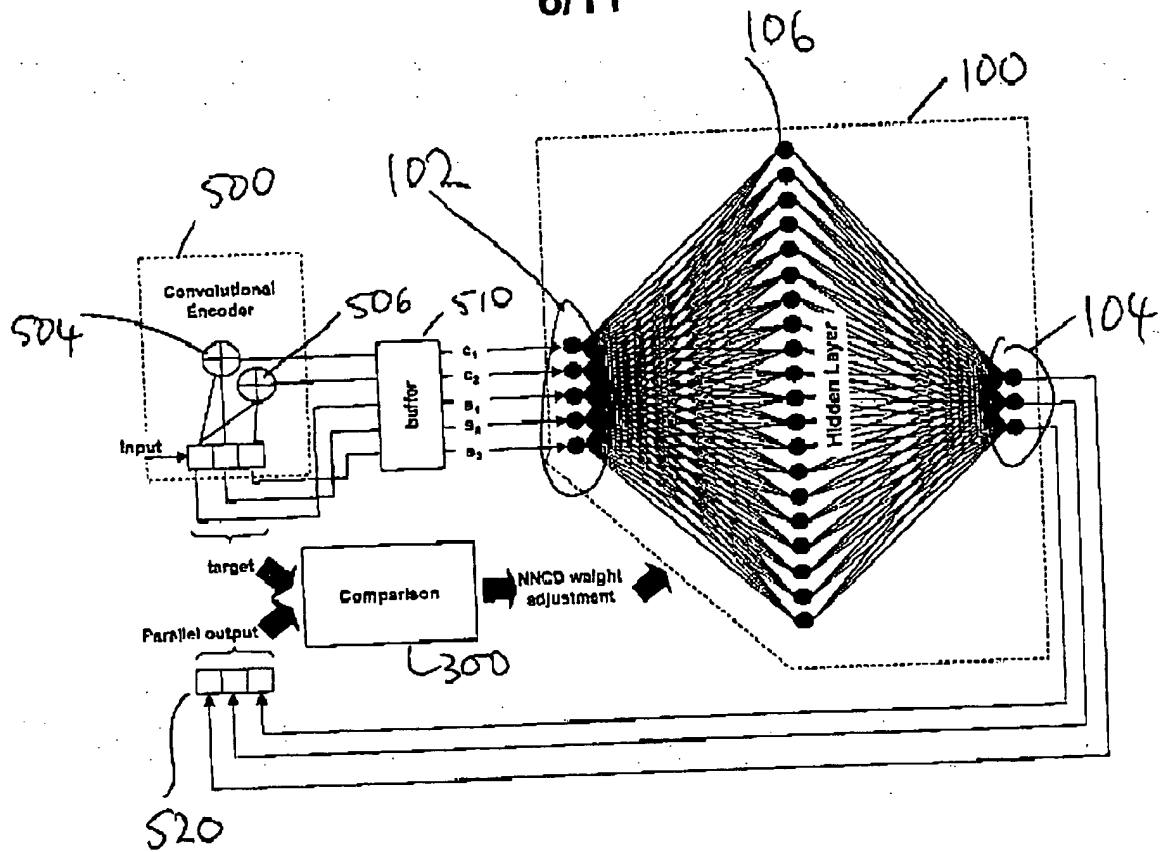
**FIG. 3**

**FIG. 4**



**FIG. 5**

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**FIG. 6**

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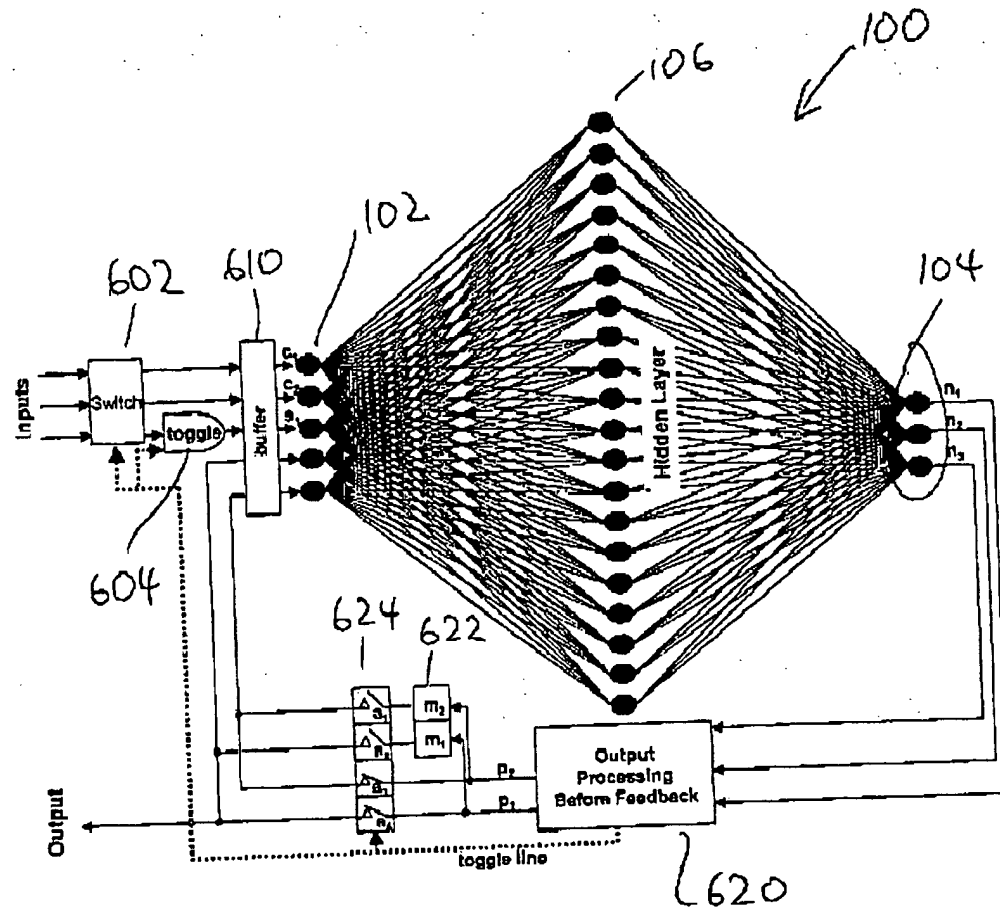


FIG. 7

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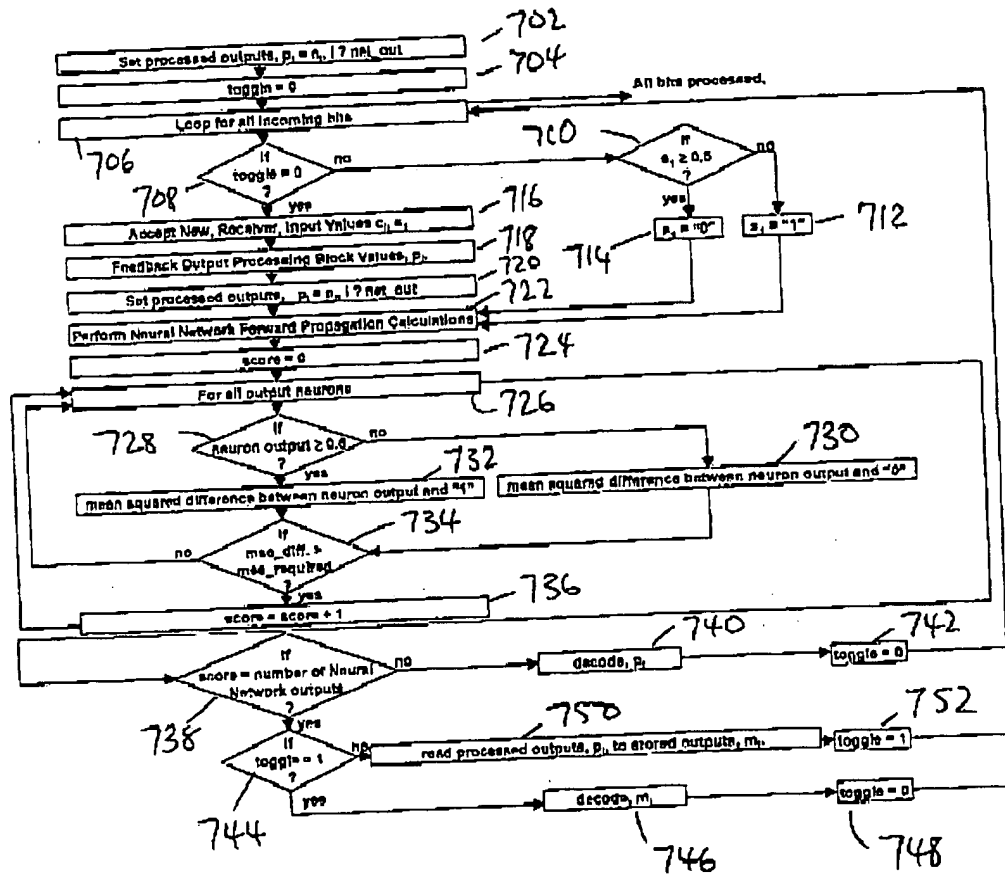
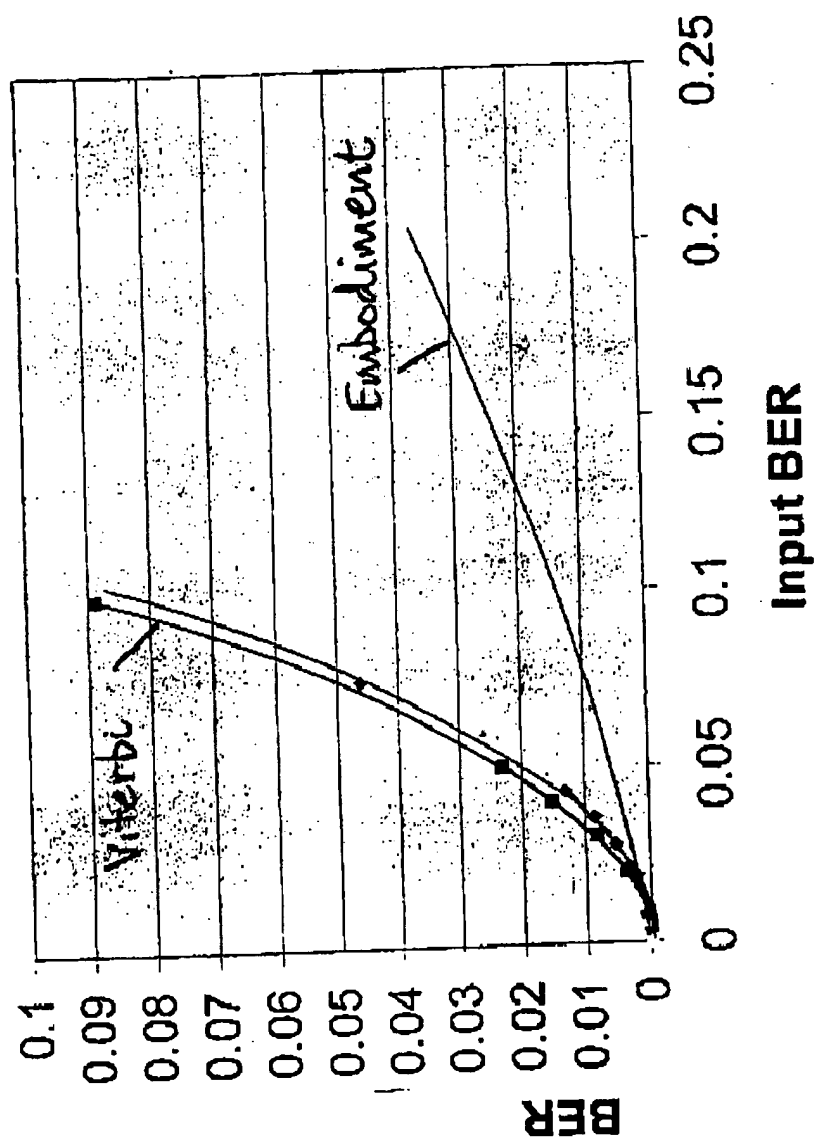


FIG. 8

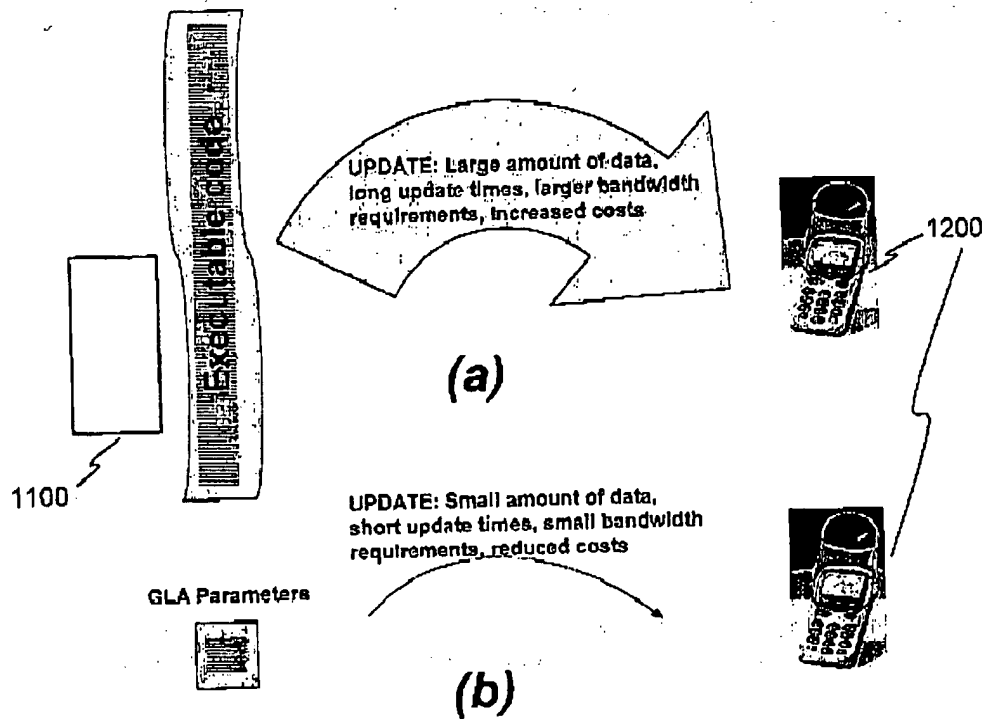


FIG. 9

# Input BER versus Output BER

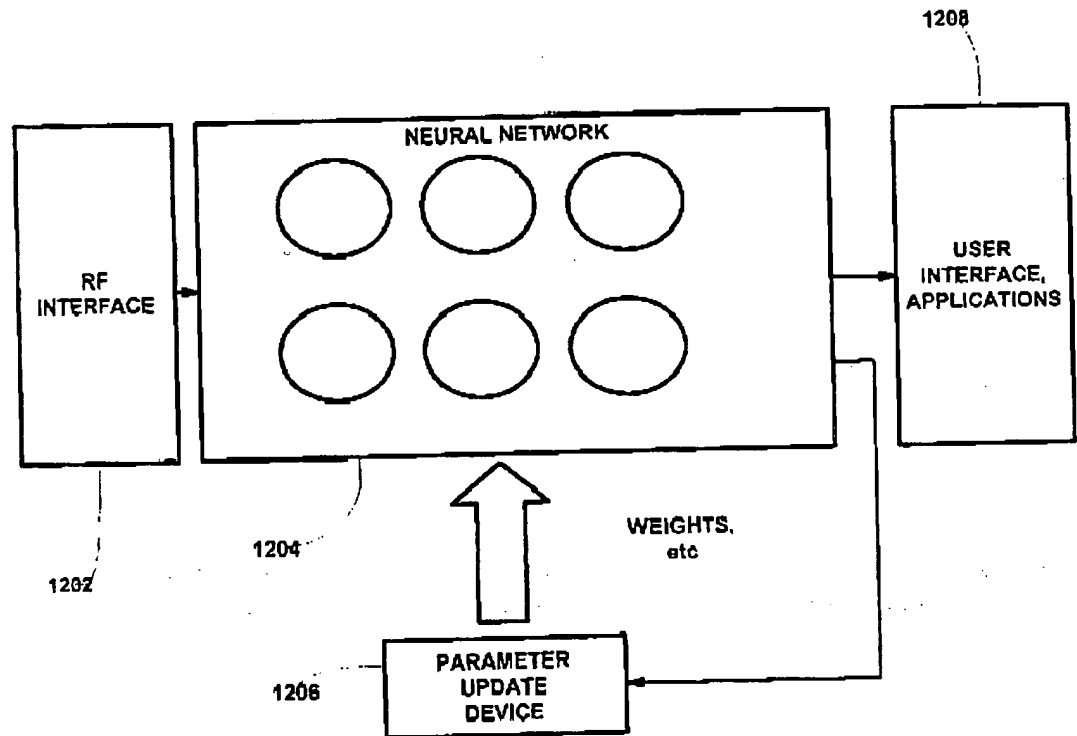


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**FIG. 10**

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**FIG. 11**